



Program Highlights			
19th December 2011 (Monday) – ISED (Ramada Resort Cochin)			
9:00AM -- 10:00AM	Registration, Breakfast		
10:00AM --11:00AM	ISED Inaugural Event	Registration, Breakfast	
11:00AM -- 12:00PM	Plenary Talk -- 1		
12:00PM --1:00PM	Analog/Mixed-Signal System Design (AMS) -- 1	Digital System Design and Validation (DSD) -- 1	Software System and Application Design (SSD) -- 1
1:00PM -- 2:00PM	Lunch		
2:00PM -- 2:30PM	Plenary Talk -- 2		
2:30PM -- 3:30PM	Analog/Mixed-Signal System Design (AMS) -- 2	Digital System Design and Validation (DSD) -- 2	Software System and Application Design (SSD) -- 2
3:30PM -- 4:00PM	Tea break		
4:00PM -- 4:30PM	Plenary Talk -- 3		
4:30PM -- 5:30PM	Analog/Mixed-Signal System Design (AMS) -- 3	Digital System Design and Validation (DSD) -- 3	Software System and Application Design (SSD) -- 3
5:30PM -- 6:00PM	Plenary Talk -- 4		
6:00PM -- 7:00PM	Break		
7:00PM -- 9:00PM	Cultural Programs	Symposium Banquet	



20th December 2011 (Tuesday) – ISED (Ramada Resort Cochin)			
9:00AM -- 10:00AM	Registration, Breakfast		
10:00AM -- 10:30AM	Plenary Talk -- 5		
10:30AM -- 11:30AM	Emerging Technology and System Design (ETD) -- 1	Digital System Design and Validation (DSD) -- 4	Embedded System Design (ESD) -- 1
11:30AM -- 12:15PM	Plenary Talk -- 6		
12:15PM -- 1:30PM	Lunch		
1:30PM -- 2:30PM	Emerging Technology and System Design (ETD) -- 2	Power-Aware System Design (PSD) -- 1	Embedded System Design (ESD) -- 2
2:30PM -- 3:15PM	Plenary Talk -- 7		
3:15PM -- 3:45PM	Tea break		
3:45PM -- 4:15PM	Plenary Talk -- 8		
4:15PM -- 5:15PM	Emerging Technology and System Design (ETD) -- 3	Power-Aware System Design (PSD) -- 2	ISED Committee Meeting
5:15PM -- 5:30PM	Tea break		
5:30PM -- 6:00PM	ISED 2011 Award Ceremony		
21st December 2011 (Wednesday) – WES (Rajagiri School of Engineering & Technology (RSET) Campus)			
9:00AM -- 10:00AM	WES Inauguration	Registration, Breakfast	
10:00AM - 11:00AM	WES -- Plenary Talk -- 9		
11:00AM -- 12:00PM	WES -- Plenary Talk -- 10		
12:00PM -- 1:00PM	WES -- Plenary Talk -- 11		
1:00PM -- 2:00PM	Lunch		
2:00PM -- 3:00PM	WES -- Plenary Talk -- 12		
3:00PM -- 4:00PM	WES -- Plenary Talk -- 13		
4:00PM -- 5:00PM	WES -- Plenary Talk -- 14		



Program Detailed Schedule			
19th December 2011 (Monday) – ISED (Ramada Resort Cochin)			
9:00AM -- 10:00AM	Registration, Breakfast		
10:00AM -- 11:00AM	ISED Inaugural Event Steering Committee Chair – 5mins, General Chair – 10mins, Program Chair – 10mins, Workshop Chair – 5mins, Remaining 30mins for other dignitaries.	Registration, Breakfast	
11:00AM -- 12:00PM	Plenary Talk – 1 Title: Design and Methodology Challenges in Next generation SoC Development Brief Abstract: goes here ... Biography: Rajat Gupta, Independent Consultant, India.		
12:00PM -- 1:00PM	Analog/Mixed-Signal System Design (AMS) – 1 Session Chair: goes here	Digital System Design and Validation (DSD) – 1 Session Chair: goes here	Software System and Application Design (SSD) – 1 Session Chair: goes here
1:00PM -- 2:00PM	Lunch		
2:00PM -- 2:30PM	Plenary Talk – 2 Title: Digital PID Control Design: A Data Based Approach Brief Abstract: goes here ... Biography: Shankar P. Bhattacharyya, Texas A & M University		
2:30PM -- 3:30PM	Analog/Mixed-Signal System Design (AMS) – 2 Session Chair: goes here	Digital System Design and Validation (DSD) – 2 Session Chair: goes here	Software System and Application Design (SSD) – 2 Session Chair: goes here
3:30PM -- 4:00PM	Tea break		
4:00PM -- 4:30PM	Plenary Talk – 3 Title: Reconfigurable Silicon Cores - Opportunities and Challenges		



International Symposium on Electronic System Design

(19–21 December 2011, Kochi, India.)

(Homepage– <http://www.ised2011.org> and <http://ised.seedsnet.org>)

	Brief Abstract: goes here ... Biography: Ranjani Narayan, Morphing Machines, India		
4:30PM -- 5:30PM	Analog/Mixed-Signal System Design (AMS) – 3 Session Chair: goes here	Digital System Design and Validation (DSD) – 3 Session Chair: goes here	Software System and Application Design (SSD) – 3 Session Chair: goes here
5:30PM -- 6:00PM	Plenary Talk – 4 Title: goes here ... Brief Abstract: goes here ... Biography: goes here ...		
6:00PM -- 7:00PM	Break		
7:00PM -- 9:00PM	Cultural Programs	Symposium Banquet	
20th December 2011 (Tuesday) – ISED (Ramada Resort Cochin)			
9:00AM -- 10:00AM	Registration, Breakfast		
10:00AM -- 10:30AM	Plenary Talk – 5 Title: 3-D Stacking for Terascale Computing Brief Abstract: goes here ... Biography: Tanay Karnik, Intel Research, USA		
10:30AM -- 11:30AM	Emerging Technology and System Design (ETD) – 1 Session Chair: goes here	Digital System Design and Validation (DSD) – 4 Session Chair: goes here	Embedded System Design (ESD) – 1 Session Chair: goes here
11:30AM -- 12:15PM	Plenary Talk -- 6 Title: CMOS Reliability: Trends and Design Techniques Brief Abstract: goes here ... Biography: Sachin Sapatnekar, University of Minnesota		
12:15PM -- 1:30PM	Lunch		



1:30PM -- 2:30PM	Emerging Technology and System Design (ETD) – 2 Session Chair: goes here	Power-Aware System Design (PSD) – 1 Session Chair: goes here	Embedded System Design (ESD) – 2 Session Chair: goes here
2:30PM -- 3:15PM	Plenary Talk -- 7		
	Title: Exploiting Unreliable Memories Brief Abstract: goes here ... Biography: Nikil Dutt, University of California, Irvine		
3:15PM -- 3:45PM	Tea break		
3:45PM -- 4:15PM	Plenary Talk – 8		
	Title: Securing Embedded Systems – Catching the Ghost in the Machine Brief Abstract: goes here ... Biography: Chandan Haldar, Morphing Machines, India		
4:15PM -- 5:15PM	Emerging Technology and System Design (ETD) – 3 Session Chair: goes here	Power-Aware System Design (PSD) – 2 Session Chair: goes here	ISED Committee Meeting
5:15PM -- 5:30PM	Tea break		
5:30PM -- 6:00PM	ISED 2011 Award Ceremony		
21st December 2011 (Wednesday) – WES (Rajagiri School of Engineering & Technology (RSET) Campus)			
9:00AM - 10:00AM	WES Inauguration	Registration, Breakfast	
	RSET Dignitaries, and ISED General and Workshop Chairs.		
10:00AM -- 11:00PM	WES -- Plenary Talk – 9		
	Title: Invent the Inventor, Create the Entrepreneur - The TreeLabs perspective in energy-efficient engineering Brief Abstract: goes here ... Biography: Dipankar, TreeLabs		
11:00PM --	WES -- Plenary Talk – 10		



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12:00PM	<p>Title: The joys and perils of a research career: my journey so far Brief Abstract: goes here ... Biography: Animashree Anandkumar, University of California, Irvine</p>
WES -- Plenary Talk – 11	
12:00PM -- 1:00PM	<p>Title: Photonics Research at IIT Madras Brief Abstract: goes here ... Biography: Shanti Bhattacharya, Indian Institute of Technology, Madras</p>
1:00PM -- 2:00PM	Lunch
WES -- Plenary Talk – 12	
2:00PM -- 3:00PM	<p>Title: Beyond Moore’s Law Technologies & Architectures Brief Abstract: goes here ... Biography: Pinaki Mazumder, University of Michigan, Ann Arbor</p>
WES -- Plenary Talk – 13	
3:00PM -- 4:00PM	<p>Title: Wireless Network Cloud: A disruptive alternative to conventional network architecture. Brief Abstract: goes here ... Biography: Parul Gupta, IBM Research India</p>
WES -- Plenary Talk – 14	
4:00PM -- 5:00PM	<p>Title: goes here ... Brief Abstract: goes here ... Biography: Vasantha Erraguntla, Intel Research, India</p>